CLAIM AMENDMENTS

11.   (Currently Amended) A system comprising:

a processor;

a storage coupled to said processor;

a video controller coupled to said processor; and

a packetization device coupled to said video controller to independently packetize at least two heterogeneous video streams for transmission thereof to a display device without converting the formats and frame rates of said at least two heterogeneous video streams to a common output format and rate [[in respective native information formats and associated native frame rates]].

12.   (Original) The system of claim 11 including a modulation device to modulate and transport said independently packetized streams.

13.   (Currently Amended) The system of claim 11 wherein each of said video streams has a different frame rate and is packetized to be de-packetized at the original it's different frame rate in a display device.